

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Hisashi OHTANI et al.

Serial No. 09/837,552

Filed: April 19, 2001

For: SEMICONDUCTOR DEVICE AND

MANUFACTURING METHOD

THEREOF



Art Unit: 2815

Examiner: N. Drew Richards

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231 on November 4, 2002
Adele M. Stamper
Adele M. Stamper

AMENDMENT

Honorable Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the non-final Office Action dated July 2, 2002, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Pursuant to the objection to the specification set forth by the Examiner, the Applicants will, upon an indication of allowable matter, file either 1) a Substitute Specification, pursuant to 37 C.F.R. 1.125, (including a marked-up version) which will contain no new matter, or 2) an amendment to the current specification, setting forth USPTO application format requested by the Examiner.

Please replace the current Abstract with the Abstract submitted on a separate sheet attached to the response.

IN THE CLAIMS: